500ADA-W

Word Clock Distribution Amplifier (1x9)

The 500ADA-W is a SDIF-2 Word Clock distribution amplifier. The input can be configured to be high impedance or terminated to 75Ω. The 500ADA-W provides continuous voltage gain adjust from -6dB to +6dB. The module supports a maximum output signal of 5V.

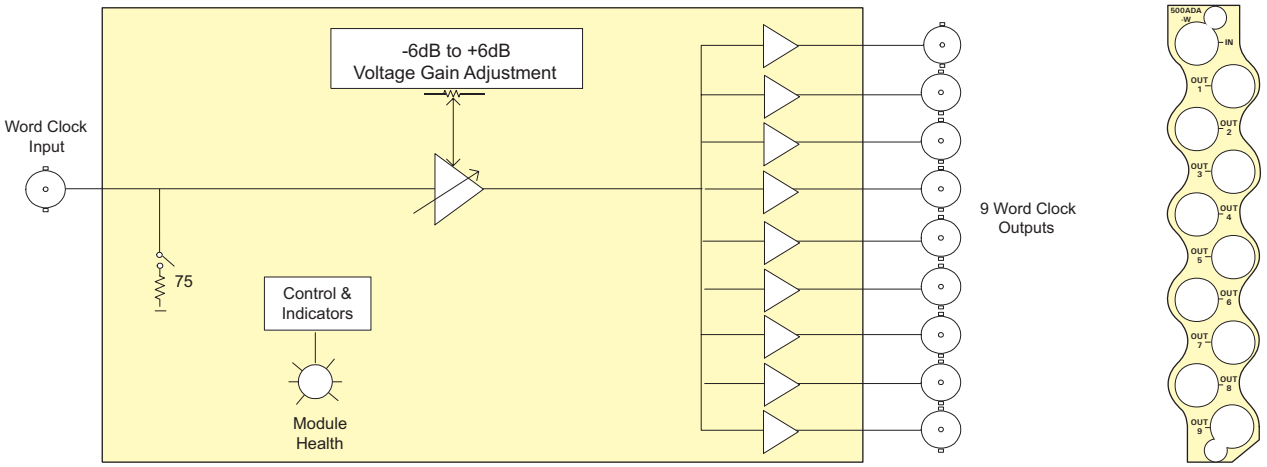
The 500ADA-W is housed in the 3RU 500FR ***exponent*** frame that will hold up to 16 modules.

Features & Benefits

- Jumper selectable 75Ω or high impedance (1kΩ typical) input
- DA has voltage gain adjustment range from -6dB to +6dB
- Outputs can drive into 75Ω loads

Card Edge LEDs

- Module status/Local Fault
- Power supply status



Specifications

Word Clock Input:

Standard:	SDIF-2 Word Clock
Number of Inputs:	1
Connector:	BNC per IEC 61169-8 Annex A
Level:	0 to 5V (terminated or unterminated)
Impedance:	Selectable 75Ω or high impedance (1kΩ typical)

Word Clock Outputs:

Number of outputs:	9
Connector:	BNC per IEC 61169-8 Annex A
Output impedance:	75Ω
Max. Output levels:	5V into 75Ω load 10V into high impedance load
Min. Output Level:	0V
Voltage Gain Range:	-6dB to +6dB
Frequency range:	28kHz-50kHz

Electrical:

Voltage:	+12V DC
Power:	1.2W
EMI/RFI:	Complies with FCC Part 15 Class A, EU EMC Directive

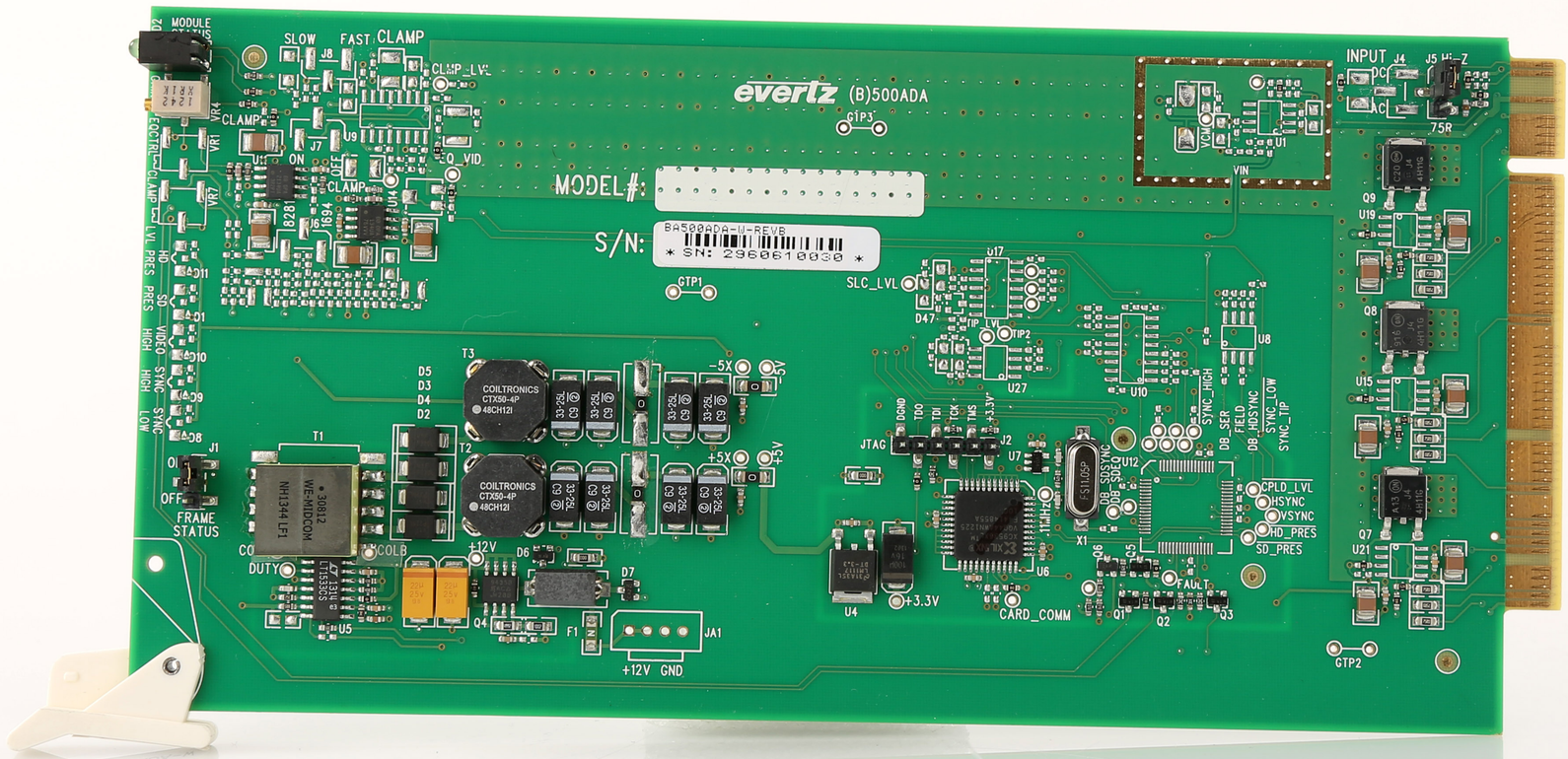
Physical:

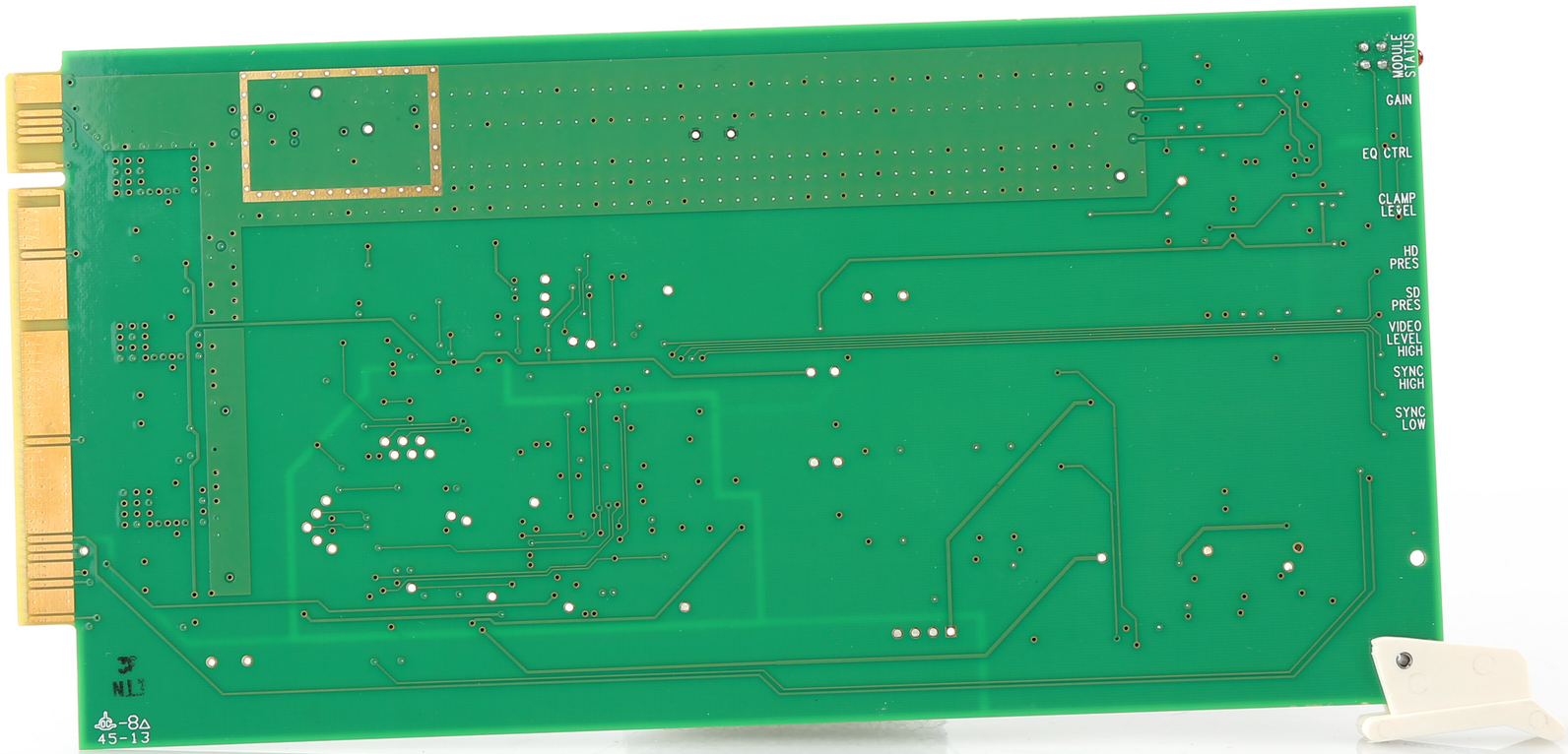
Number of Slots:	1

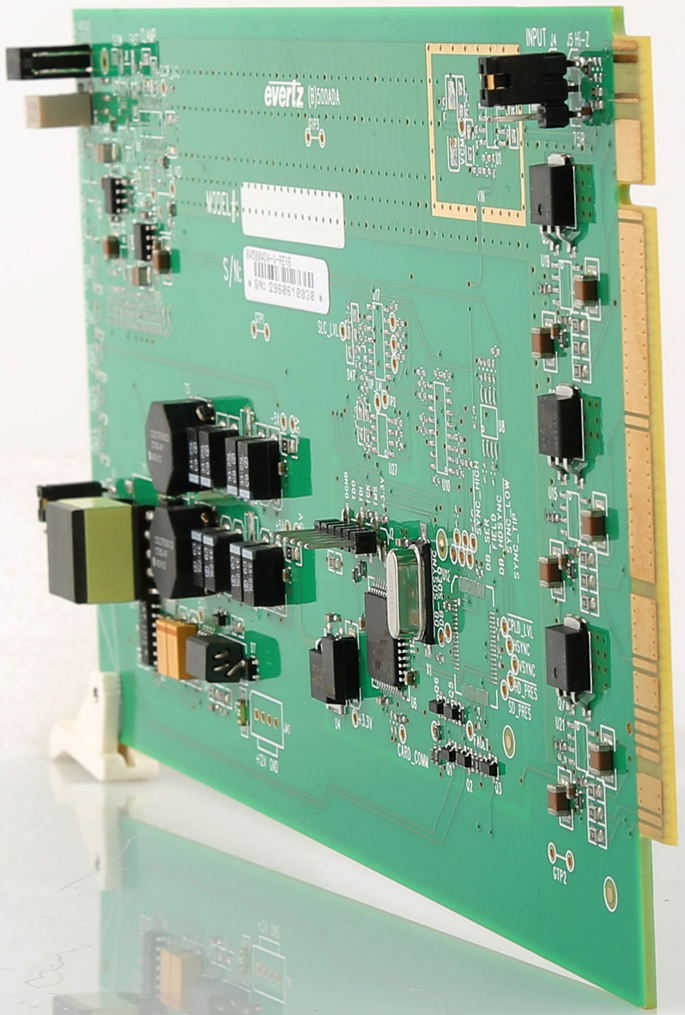
Ordering Information

500ADA-W	Word Clock Distribution Amplifier (1x9)

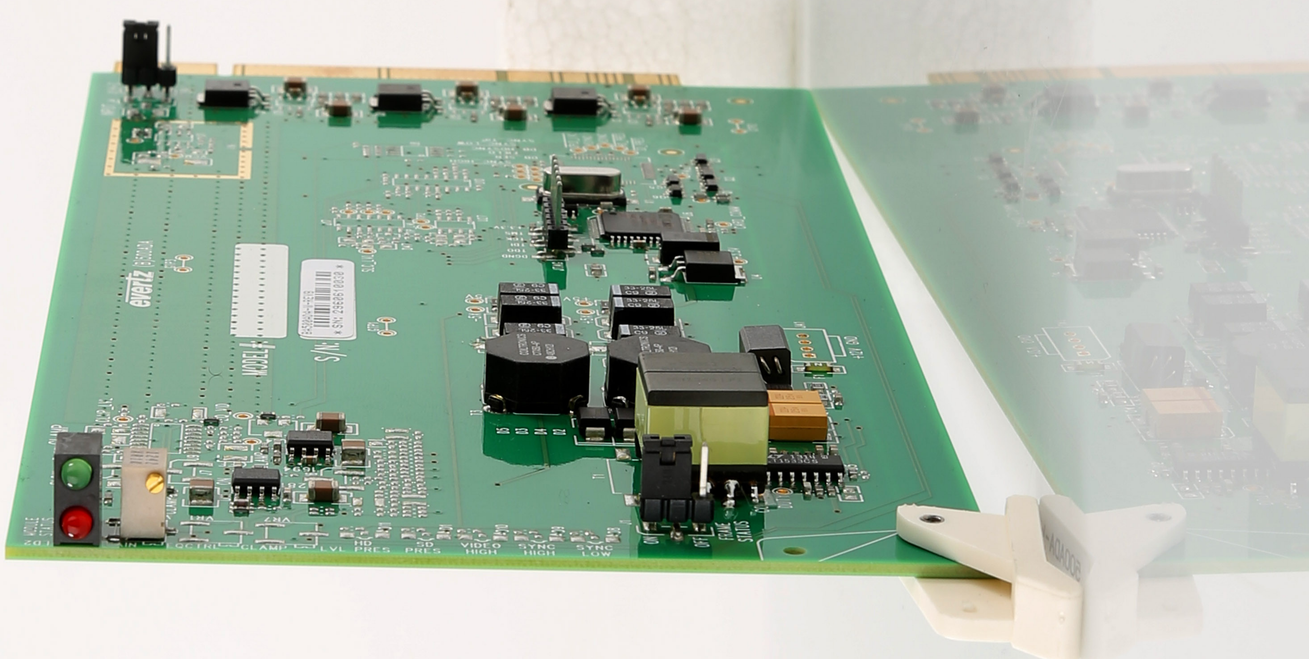
Enclosures	***exponent***
500FR	Compact High Density Distribution Frame
S501FR	Standalone enclosure

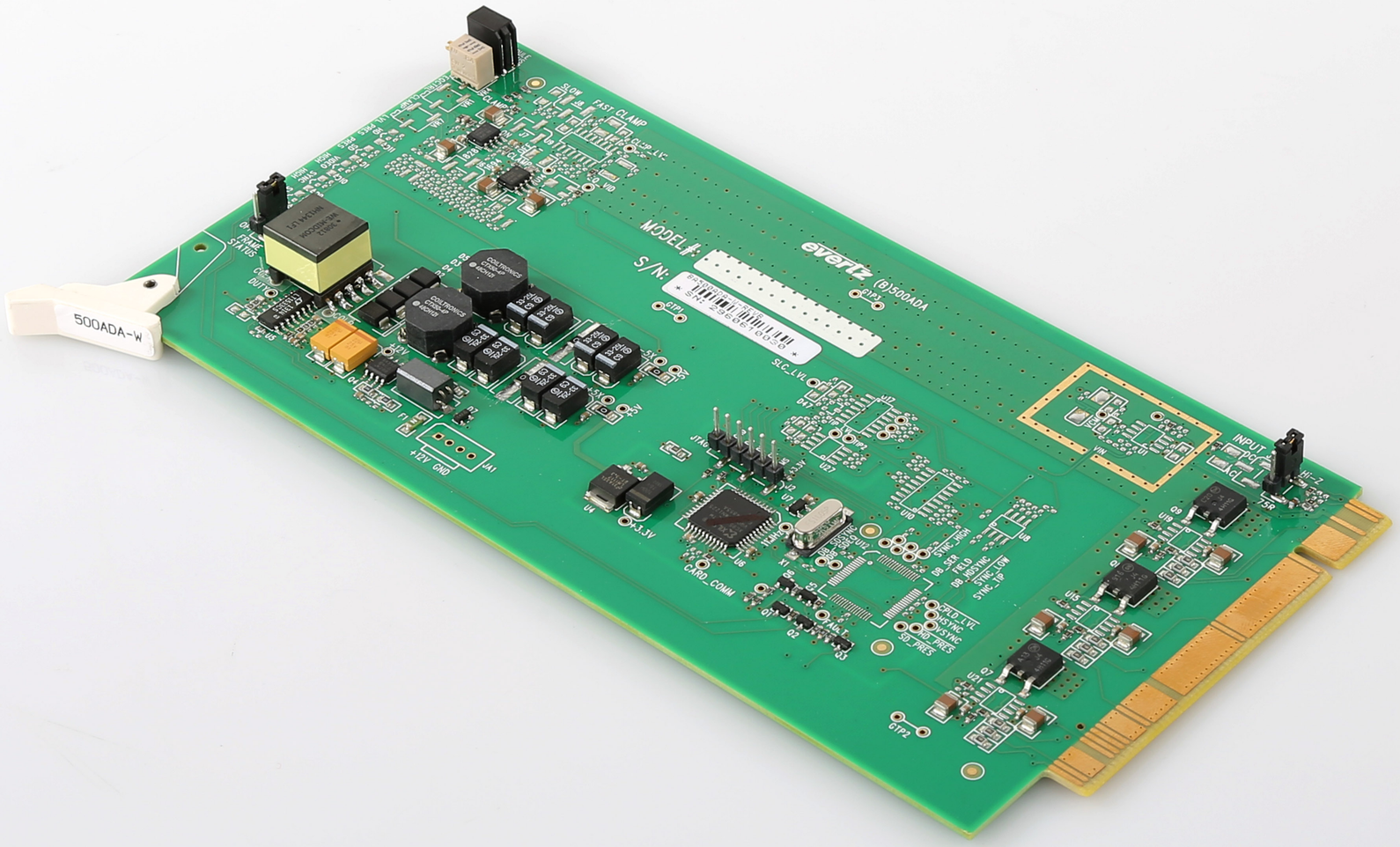

MODULE STATUS
SLOW
FAST
CLAMP
CLAMP_LVL
EQ_VID
CLAMP
ON
OFF
CLAMP
EQCTRL
CLAMP
LVL PRES
HD
SD
VIDEO
SYNC
HIGH
HIGH
LOW
SYNC
everlz (B)500ADA
MODEL#:
S/N:
BA500ADA-U-REVB
* SN: 296061003 *
GTP1
GTP3
SLC_LVL
D47
TP1
TP2
U27
U17
U8
U10
T3
COILTRONICS
CTX50-4P
48CH12I
-5X
5V
D5
D3
D4
D2
T1
T2
+5X
+5V
J1
OFF
FRAME
STATUS
DUTY
COLB
+12V
D6
D7
F1
JA1
+12V GND
U5
Q4
U4
+3.3V
JTAG
DGND
TDO
TDI
TCK
TMS
+3.3V
J2
U7
U12
SYNC_HIGH
DB_SER
FIELD
DB_HSYNC
SYNC_LOW
SYNC_TIP
CPLD_LVL
HSYNC
VSYNC
HD_PRES
SD_PRES
U6
X1
FAULT
CARD_COMM
Q1
Q2
J3
INPUT
J4
J5
Hi-Z
75R
VIN
U1
Q9
U19
Q8
U15
Q7
U21
GTP2

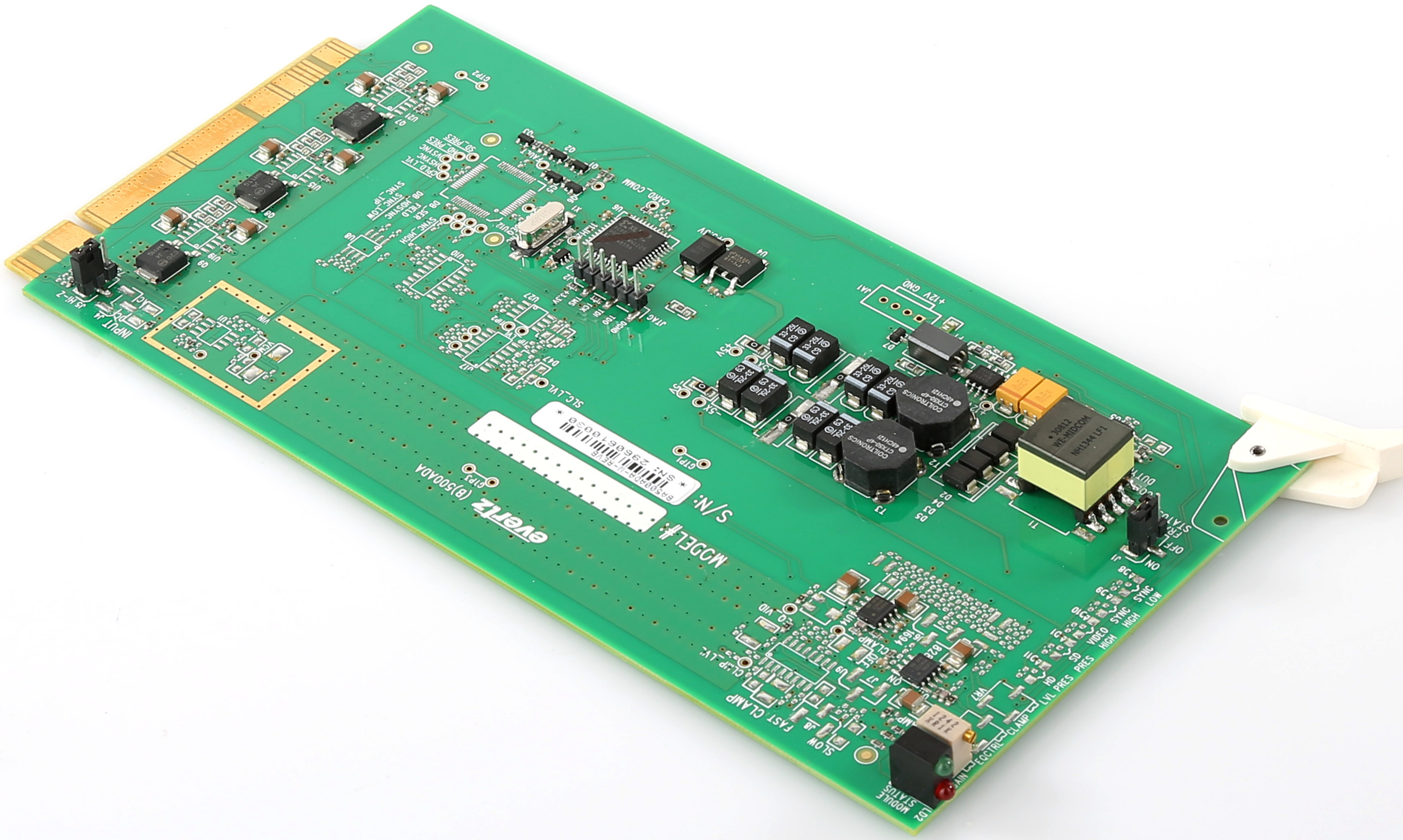

MODULE STATUS
GAIN
EQ CTRL
CLAMP LEVEL
HD PRES
SD PRES
VIDEO LEVEL HIGH
SYNC HIGH
SYNC LOW
45-13


everlz
INPUT
SYNC_PRES
VSYNC
SDI_PRES

GAIN
EQ CTRL
CLAMP LEVEL
HD PRES
SD PRES
VIDEO LEVEL HIGH
SYNC HIGH
SYNC LOW




500ADA-W
everlz
(8)500ADA
MODEL#
S/N:
FAST CLAMP
SLOW
SLC_LVL
INPUT
Hi-Z
75R
12V GND
3.3V
CARD_COMM
SYNC_ON
DB_SCR
FIELD
DB_ADSYNC
SYNC_LOW
SYNC_UP
CPLD_2.5V
VSYNC
HSYNC
GND_PRES
5V_PRES
JTAG
J1P1
J2P1
J3P1
J4P1
J5P1
J6P1
J6P2


S/N:
MODEL#:
evertz