

520DARS-W

Unbalanced AES Word Clock Extractor Audio Distribution Amplifier



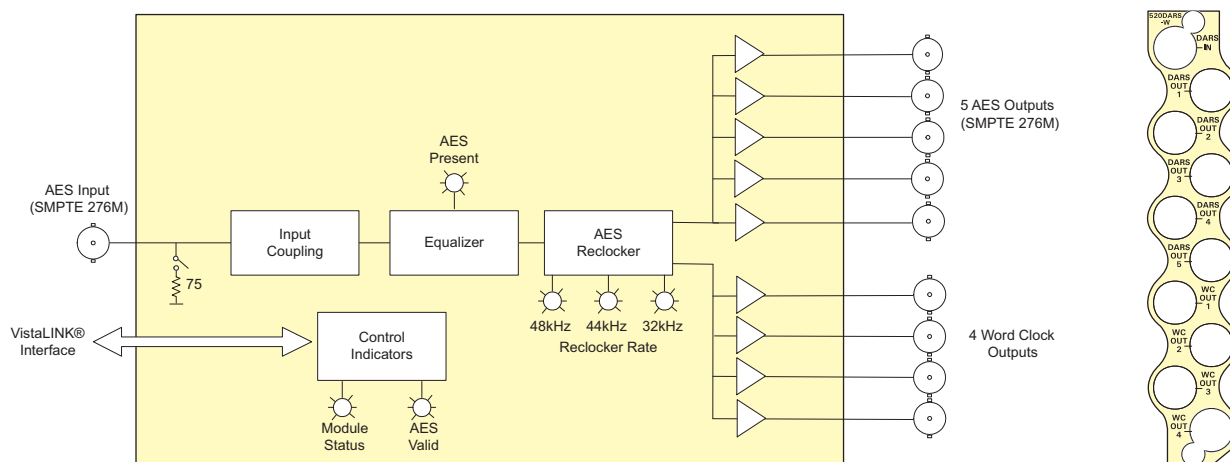
The 520DARS-W provides a compact method of extracting word clock from your AES digital audio reference signals. The 520DARS-W features one auto-equalized input with 4 word clock outputs and 5 reclocked AES audio outputs.

The 520DARS-W is housed in the 3RU 500FR **exponent** Frame that will hold up to 16 modules.

The 520DARS-W can be used in conjunction with the 5600MSC Master Clock/SPG system.

Features & Benefits

- Supports AES audio over 75Ω coax (SMPTE 276M)
- 4 word clock outputs (AES11-2003)
- 5 reclocked AES outputs provides jitter reduction (SMPTE 276M)
- Automatic equalization provides extended cable length capabilities
- High impedance or 75Ω termination on input (jumper selectable)
- Card edge indicators for AES present, reclocker rate, and AES validity bit
- Tally output of input error conditions
- VistaLINK® capable for remote monitoring via SNMP (using VistaLINK® PRO) when installed in 500FR frame with 500FC VistaLINK® Frame Controller



Specifications

AES Input:

Standard: SMPTE 276M
Number of Inputs: 1
Connector: BNC per IEC 60169-8 Amendment 2
Input Level: 1V p-p
Coupling: Transformer
Input Impedance: 75Ω
Return Loss: > 25dB 100kHz to 6MHz
Equalization: Automatic to 1500m with Belden 1694A (or equivalent) @ 48kHz AES signal

Sampling Frequency: 32kHz, 44.1kHz, 48kHz and 96kHz

AES Output:

Number of Outputs: 5 Unbalanced AES
Connector: BNC per IEC 60169-8 Amendment 2
Output Level: 1V p-p
Output Impedance: 75Ω
Return Loss: > 25dB 100kHz to 6MHz

Word Clock Outputs:

Standard: AES11-2003
Number of Outputs: 4
Connectors: BNC per IEC 169-8
Signal Level: 5V p-p square wave (0-5V) ±0.5V
Physical:

Number of Slots: 1

Electrical:

Voltage: +12V DC
Power: 5W
EMI/RFI: Complies with FCC Part 15 Class A EU EMC Directive

Ordering Information

520DARS-W Unbalanced AES Word Clock Extractor Audio Distribution Amplifier

Enclosures
500FR
S501FR

exponent
Compact High Density Distribution Frame
Standalone enclosure